U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

FUJITSU LIMITED, 1015, Kamikodanaka, Nakahara-ku, Kawasaki-shi, Kanagawa, 211

	(hereinafter ASSIGNEE), the receipt of which is hereby transfers to ASSIGNEE the entire and exclusive right, ti	<u> </u>	•
Fitle of overtion)	METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND APPARATUS USED IN SUCH METHOD		
	for which application for Letters Patent of the United States was		
Check	2 executed on even date herewith,		
·Box)	🗆 filed on, Serial No.	,	N 0
•	and all Letters Patent of the United States to be obtained therefor on said application or any continuation, division, renewal, substitute reissue or reexamination thereof for the full term or terms for which the same may granted.		
	The ASSIGNOR agree(s) to execute all papers necessary in connection with this application and any continuing,—divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.		
	with such applications as the ASSIGNEE may deem nece	essary or expedient.	
	with such applications as the ASSIGNEE may deem necessar may be declared concerning this application or any contents. Patent or reissue patent issued thereon and to coobtaining and producing evidence and proceeding with st	ry in connection with any interference tinuation, division, reissue or reexa properate with the ASSIGNEE in ex	mination thereof or
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